

## INVENTOR INFORMATION

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## CORRESPONDENCE INFORMATION

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## APPLICATION INFORMATION

Title Line One:: Apparatus and Method for Delay Matching  
Title Line Two:: of Full and Divided Clock Signals  
Total Drawing Sheets:: 9  
Formal Drawings?: Yes  
Application Type:: Utility  
Docket Number:: 1875.1710002  
Secrecy Order in Parent Appl.?: No

## REPRESENTATIVE INFORMATION

Representative Customer Number:: 26111

## CONTINUITY INFORMATION

This application is a:: CONTINUATION OF  
> Application One:: 10/197,843  
Filing Date:: 07-19-2002  
Patent Number:: 6,597,216

Which is a::CONTINUATION OF  
>> Application Two:: 09/950,572  
Filing Date:: 09-13-2001  
Patent Number:: 6,424,190

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